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Pat Slaback (Depositor's name)
Pat Slaback (Signature)
January 28, 2004 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/090,239	03/01/2002	Arul V. Ghia	X-1061 US	6539

TITLE OF INVENTION: LOW JITTER CLOCK FOR A PHYSICAL MEDIA ACCESS SUBLAYER ON A FIELD PROGRAMMABLE GATE ARRAY

APPLN. TYPE	SMALL ENTITY	ISSUE FES	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$0	\$1330	01/28/2004
EXAMINER	ART UNIT	CLASS-SUBCLASS			
TRAN, ANHQ	2819	326-041000			

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E. Eric Hoffman
2
3

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(B) RESIDENCE: (CITY and STATE OR COUNTRY)

XILINX, INC.

San Jose, California 95124

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(Date) 1/28/04

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PTOL-85 (Rev. 10/03) Approved for use through 04/30/2004.

OMB 0651-0033 U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

PAGE 2/2 * RCVD AT 1/28/2004 12:40:51 PM [Eastern Standard Time] * SVR:USPTO-EFXXF-2/1 * DNIS:7464000 * CSID:408 377 6137 * DURATION (mm-ss):00-42

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Inventor(s): Atul V. Ghia et al.

Assignee: **XILINX, INC.**

Serial No.: 10/090,239

conf.no. 6539

Filed: March 1, 2002

Title: Low Jitter Clock for a Physical Media Access Sublayer on a Field Programmable Gate Array

Docket No.: **X-1061 US**

Enclosed: Return Receipt Postcard
Fee Transmittal - Part B

Date: January 28, 2004

VIA FACSIMILE 703-746-4000

Atty/Sec: KK/pbs